

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Johannes KRUL, et al.

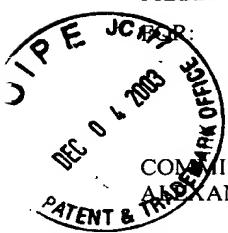
SERIAL NO: 09/692,371

GAU: 2876

FILED: October 20, 2000

EXAMINER: FUREMAN, JARED

SUBSTRATE WHICH IS MADE FROM PAPER AND IS PROVIDED WITH AN INTEGRATED CIRCUIT



INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

- Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 198707US0X CONT		SERIAL NO. 09/692,371	
LIST OF REFERENCES CITED BY APPLICANT		APPLICANT Johannes KRUL, et al.					
		FILING DATE October 20, 2000		GROUP 2876			
<b>U.S. PATENT DOCUMENTS</b>							
<i>OCT 6 2003 EXAMINER INITIALS PATENT &amp; TRADEMARK OFFICE</i>	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE	
AA	5,108,819	04-28-92	HELLER, ET AL.				
AB	5,347,144	09-13-94	GARNIER, ET AL.				
AC	5,566,441	10-22-96	MARSH, ET AL.				
AD							
<b>FOREIGN PATENT DOCUMENTS</b>							
	DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES      NO			
AE	196 30 648 A1	02-05-98	GERMANY			X	
AF	0 788 075 A2	08-06-97	EUROPE			X	
AG	99/30432	06-17-99	WIPO			X	
AH	96/08047	03-14-96	WIPO			X	
AI	2049365	09-30-03	RU (equivalent to U.S.5,108,819)			X	
AJ							
AK							
AL							
<b>OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
AM	"Currency with an Integrated Chip", IBM Technical Disclosure Bulletin, Volume 32, No. 5A, October 1989, Page 427.						
AN	"Pentacene Thin Film Transistors and Inverter Circuits", 1997 International Electron Devices Meeting Technical Digest, December 1997, Pages 539-542.						
AO	"New plastic circuits are flexible enough to be folded in half", Press Information, December 8, 1997.						
AP	May, "Polymer electronics – fact or fantasy?", Physics World, March 1995, Pages 52-57.						
AQ	The first all-polymer field effect transistor", Design Engineering, May 1995, Pages 47-48.						
AR	Conference Papers EETimes of December 9, 1997, Internet Disclosure, Development of Philips.						
AS	Leeuw, et al., "Polymeric Integrated Circuits and Light-Emitting Diodes", Philips Research Laboratories, 1997, Pages 331-336.						
AT	Brown, et al., "Logic Gates Made from Polymer Transistors and Their Use in Ring Oscillators", Science, Volume 270, November 10, 1995, Pages 972-974.						
AU	Bonnoit, et al., "Evaluation des performances de circuits logiques réalisés à partir de transistors MIS organiques", L'Onde Electrique – Juillet-Août, 1994, Volume 74, No. 4, Pages 18-24.						
AV	Communication of a Notice of Opposition Against the Equivalent European Patent 1073993, Filed By De La Rue International Limited.						
AW	Communication of a Notice of Opposition Against the Equivalent European Patent 1073993, Filed By Giesecke & Devrient GmbH.						
AX				<input type="checkbox"/> Additional References sheet(s) attached			
Examiner				Date Considered			

\*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.